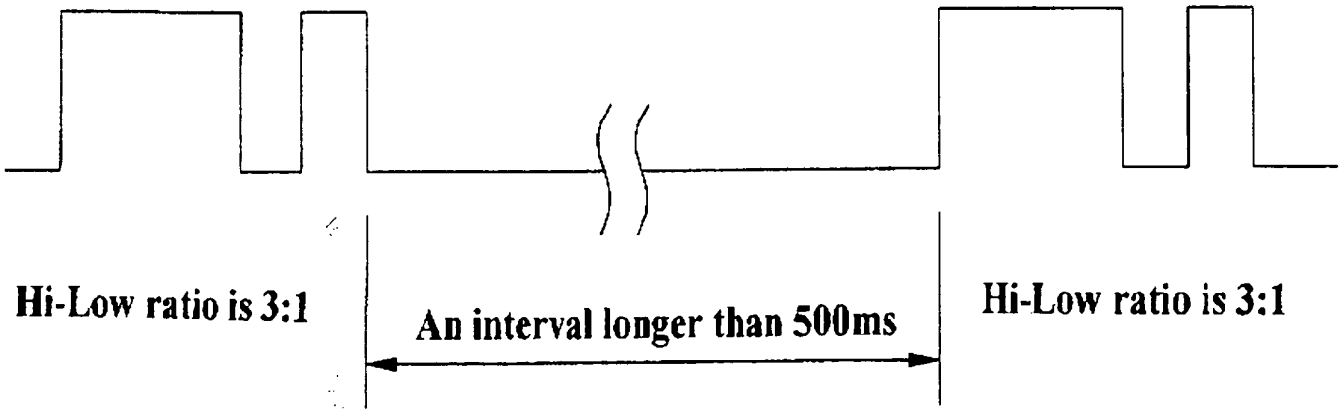

FIG.1
(PRIOR ART)

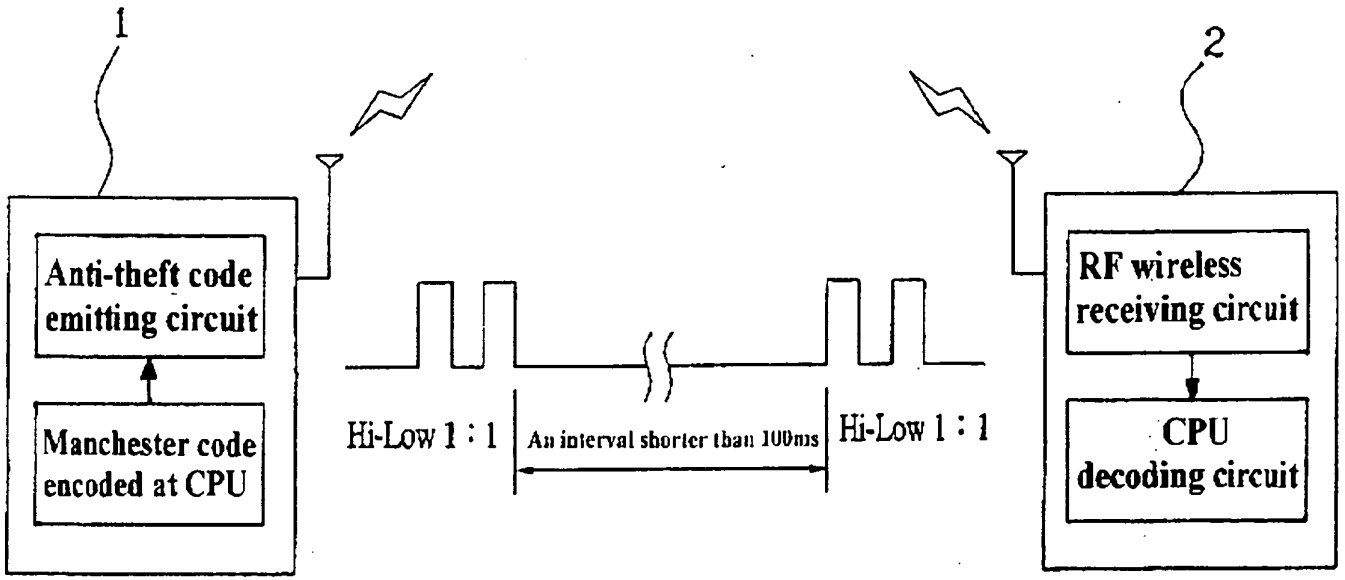
1
2
Anti-theft code emitting circuit
Manchester code encoded at CPU
Hi-Low 1 : 1
An interval shorter than 100ms
Hi-Low 1 : 1
RF wireless receiving circuit
CPU decoding circuit

FIG.2

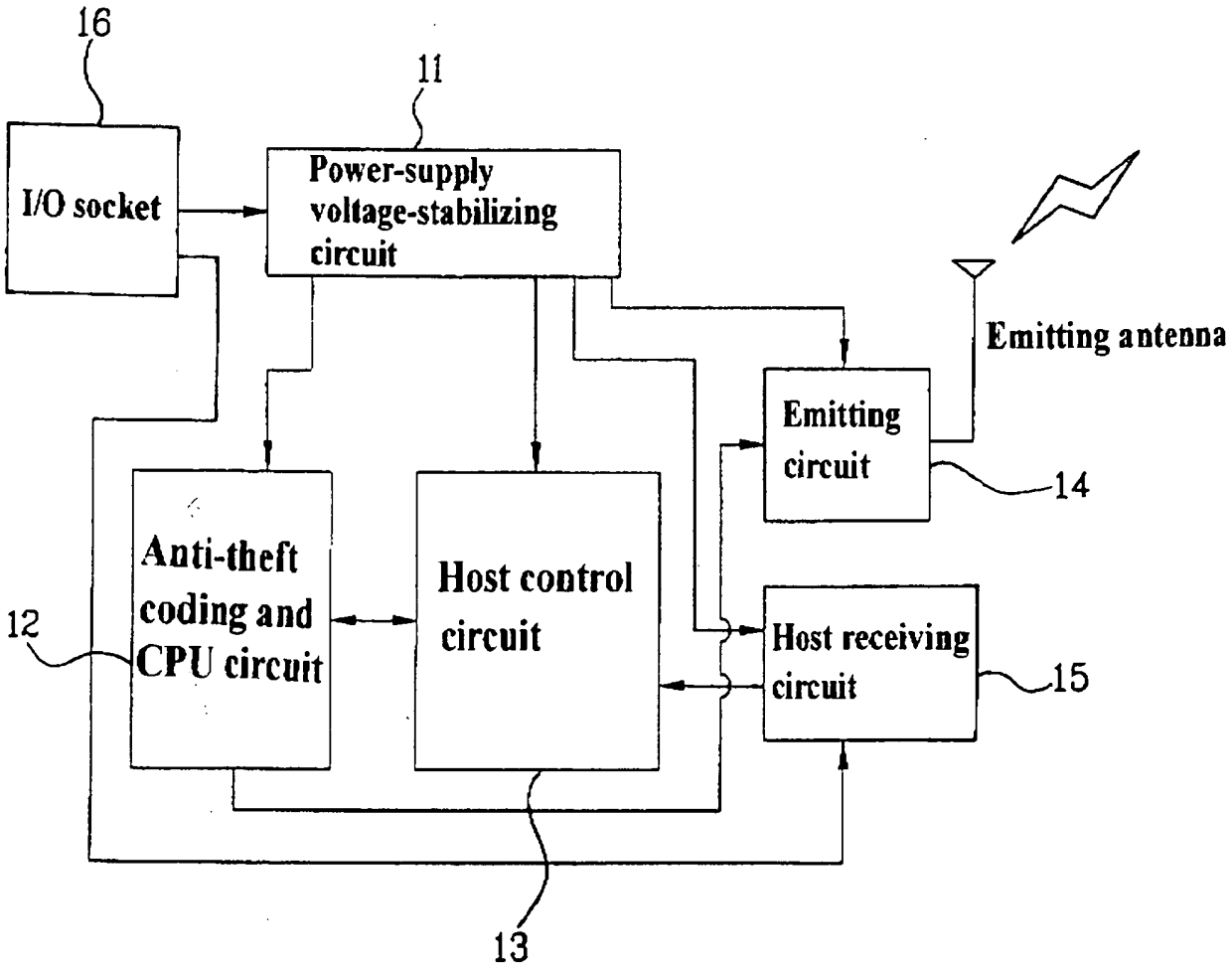
16
11
I/O socket
Power-supply voltage-stabilizing circuit
Emitting antenna
Emitting circuit
14
Anti-theft coding and CPU circuit
12
Host control circuit
Host receiving circuit
15
13

FIG. 3

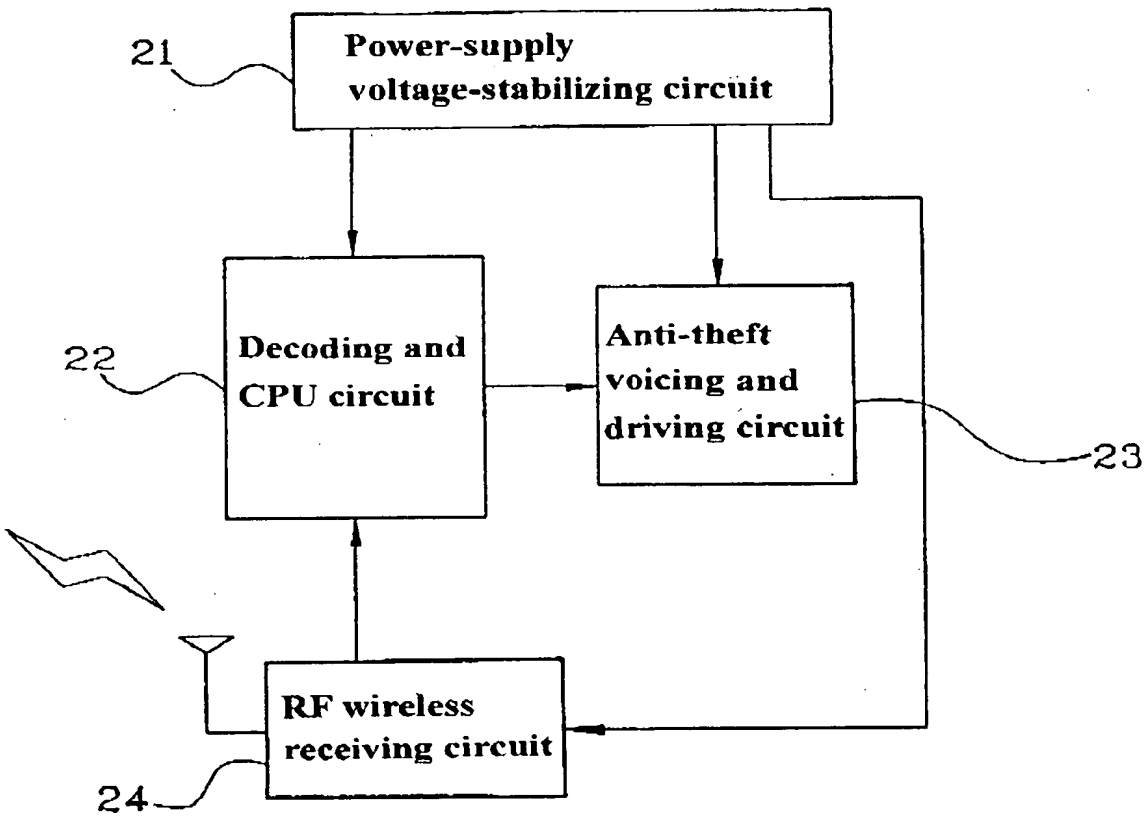
21
Power-supply
voltage-stabilizing circuit
22
Decoding and
CPU circuit
Anti-theft
voicing and
driving circuit
23
RF wireless
receiving circuit
24

FIG.4

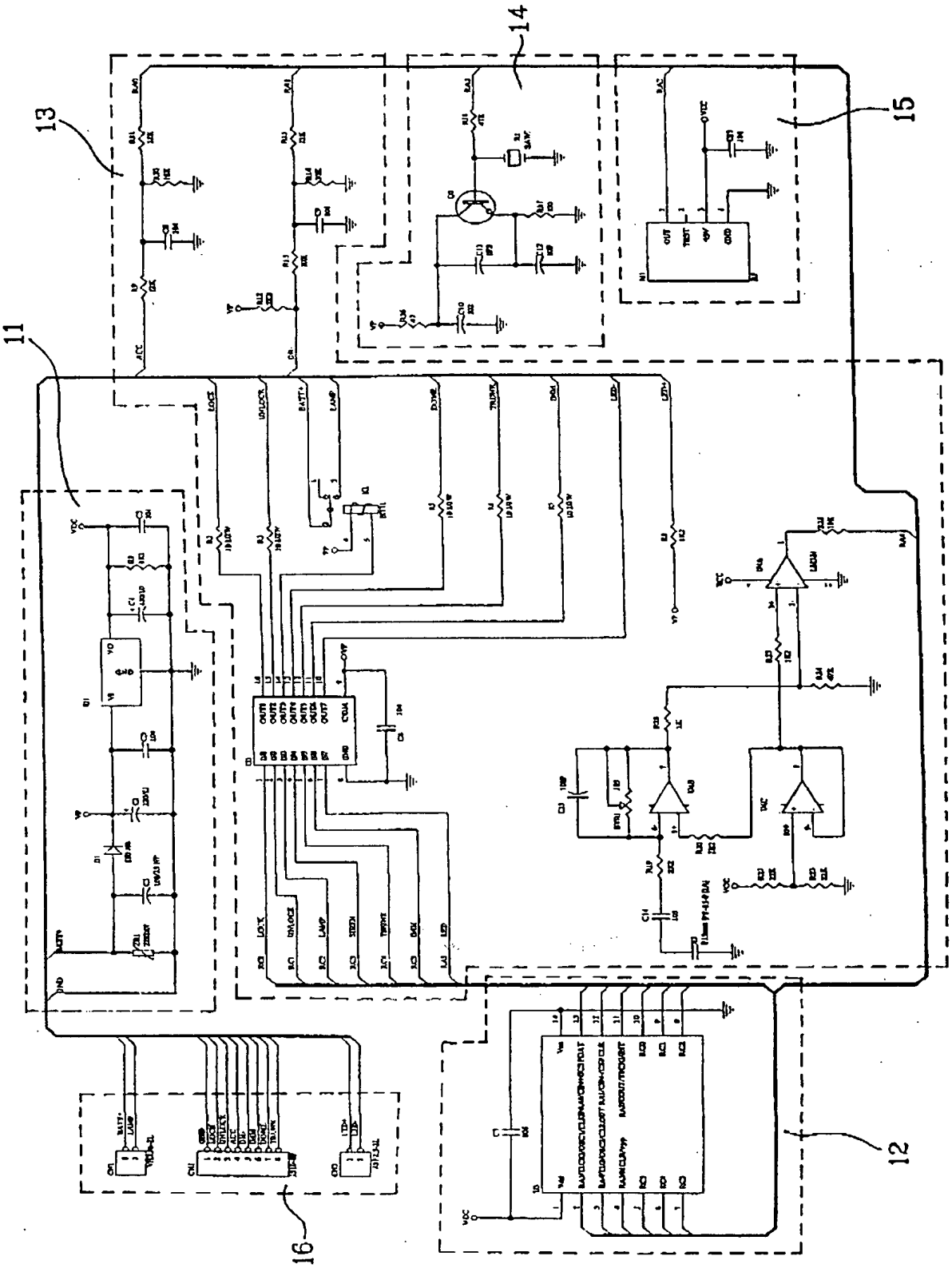

FIG.5

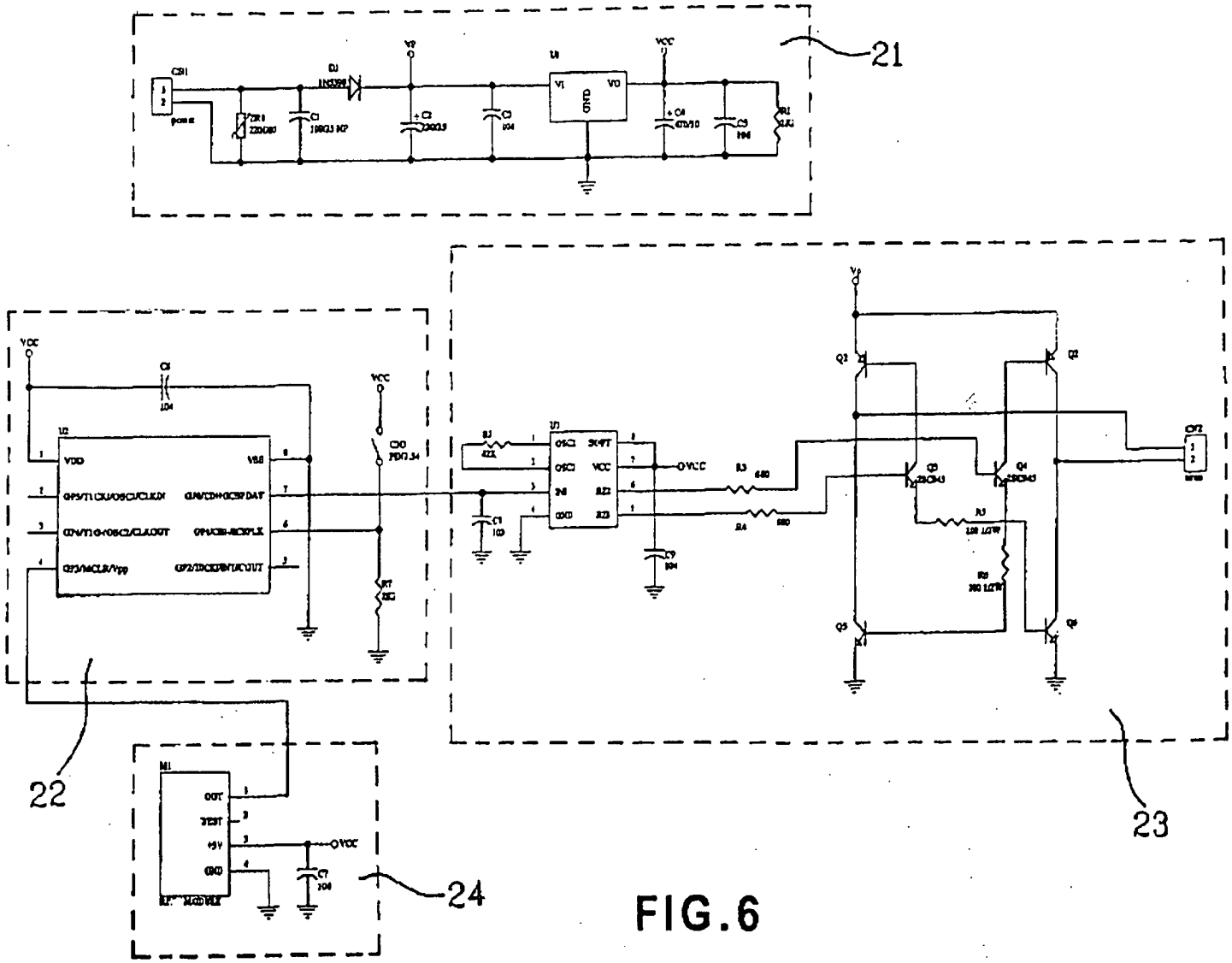

FIG.6